

Applicant: Leonard Forbes et al.
 Title: MEMORY DEVICE
 Docket No.: 303.356US1
 Filed: July 29, 1997
 Examiner: George Eckert II



Serial No.: 08/902,133
 Due Date: N/A
 Group Art Unit: 2815

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 3 cited documents.
- ☒ A Communication Concerning Co-Related Applications (3 pgs.)

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
 Customer Number 21186

By: David R. Cochran
 Atty: David R. Cochran
 Reg. No. 46,632

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of March, 2004.

MEREDITH MESCHER
 Name

Meredith Mescher
 Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
 (GENERAL)

S/N 08/902133



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.
Serial No.: 08/902,133
Filed: July 29, 1997
Title: MEMORY DEVICE

Examiner: George Eckert II
Group Art Unit: 2815
Docket: 303.356US1

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,


LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 371-2157


Date 19 MARCH 2004

By


David R. Cochran
Reg. No. 46,632

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MEREDITH MESCHER
Name


Signature

S/N 08/902133

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: George Eckert II

Serial No.: 08/902133

Group Art Unit: 2815

Filed: July 29, 1997

Docket: 303.356US1

Title: MEMORY DEVICE

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
08/903,452	July 29, 1997	303.324US1	TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE
09/256,643	February 23, 1999	303.324US2	TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE
09/652,420	August 31, 2000	303.324US3	TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE
09/691,004	October 18, 2000	303.324US4	TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE
08/903,486	July 29, 1997	303.326US1	SILICON CARBIDE GATE TRANSISTOR
09/259,870	March 1, 1999	303.326US2	FABRICATION OF SILICON CARBIDE GATE TRANSISTOR
08/902,132 5,886,368	July 29, 1997	303.353US1	TRANSISTOR WITH SILICON OXYCARBIDE GATE AND METHODS

OF FABRICATION AND USE

09/138,294 6,309,907	August 21, 1998	303.353US2	TRANSISTOR WITH SILICON OXYCARBIDE GATE AND METHODS OF FABRICATION AND USE
08/902,843	July 29, 1997	303.354US1	DEAPROM HAVING AMORPHOUS SILICON CARBIDE GATE INSULATOR
09/135,413	August 14, 1998	303.354US2	METHOD FOR OPERATING A MEMORY DEVICE HAVING AN AMORPHOUS SILICON CARBIDE GATE INSULATOR
09/134,713	August 14, 1998	303.354US3	DEAPROM HAVING AMORPHOUS SILICON CARBIDE GATE INSULATOR
08/902,098 6,031,263	July 29, 1997	303.355US1	DEAPROM AND TRANSISTOR WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE
09/140,978 6,307,775	August 27, 1998	303.355US2	DEAPROM AND TRANSISTOR WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE
09/141,392 6,249,020	August 27, 1998	303.355US3	DEAPROM AND TRANSISTOR WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE
09/883,795	June 18, 2001	303.355US4	METHOD OF FORMING A DEVICE WITH A GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE
10/047,181	October 23, 2001	303.355US5	MEMORY DEVICE WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE
10/231,687	August 29, 2002	303.356US2	MEMORY DEVICE
08/903,453	July 29, 1997	303.378US1	CARBURIZED SILICON GATE INSULATORS FOR INTEGRATED

CIRCUITS

09/258,467	February 26, 1999	303.378US2	CARBURIZED SILICON GATE INSULATORS FOR INTEGRATED CIRCUITS
09/650,553	August 30, 2000	303.378US3	CARBURIZED SILICON GATE INSULATORS FOR INTEGRATED CIRCUITS
10/461,593	June 11, 2003	303.356US3	MEMORY DEVICE
10/789,203	February 27, 2004	303.356US4	OPERATING A MEMORY DEVICE

Respectfully submitted,

LEONARD FORBES ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 371-2157

Date

19 MARCH 2004

By

David R. Cochran
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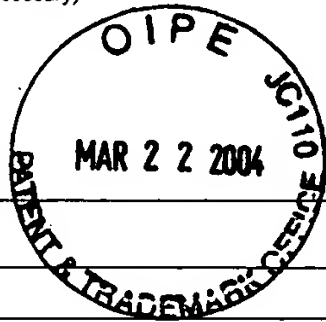
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MEREDITH MESCHER
Name

Meredith Mescher
Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	<i>Complete if Known</i>	
	Application Number	08/902,133
	Filing Date	July 29, 1997
	First Named Inventor	Forbes, Leonard
	Group Art Unit	2815
	Examiner Name	Eckert II, George
Sheet 1 of 1	Attorney Docket No: 303.356US1	



US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,336,361	08/09/1994	Tamura, A. , et al.	438	767	11/02/1992
	US-5,990,531	11/23/1999	Taskar, N. R., et al.	257	410	11/12/1997
	US-6,166,768	12/26/2000	Fossum, , et al.	348	308	01/22/1997

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached